Physical Address:

A physical address is a memory address that is represented in the form of a binary number on the address bus in order to enable the data bus to access a particular storage cell of main memory or a register of memory-mapped I/O device.

Logical Address:

When CPU divides the data into two parts to access the data in offset register it is known as the logical address.

A programmer can see offset address.

Formula:

1. Segment Address + 0
2. 0 + Offset Address

